Innovate Canada 2008 Contest

Project Paper Submission Template

Importance Notice	1) **Submission Deadline** I) Preliminary Paper Submission Deadline: **May 15, 2008** II) Final Project Paper Submission Deadline: **August 7, 2008** *Please log in www.innovatecanada.com with your ID number and Submit the paper before the deadline. *Paper revise is not allowed after the deadline. 2) **Deliverables**: Design Contest participants are required to submit: I) Preliminary Paper: Complete **Part I、IV、VI** before May 15**;** Complete **Part II** will have a bonus point. II) Final Project Paper: All sections are required. 3) Altera, the sponsor of the contest, holds the copyright and the usufruct of all the designs and their documents of this contest. 4) Each team member should sign and return **Innovate Nordic 2008 Contest Participant Release Form.**

PART I *****Design Introduction（Preliminary Paper）：
Please give some general information of your design, e.g. purpose of the design, application scope, and targeted users. Please also include a detailed description of why you used Altera FPGA devices to do the design.

PART II Function Description（Final Project Paper）：
Please give detailed information to show the functionality of your design and how to implement it.

PART III   Performance Parameters（Final Project Paper）：

Please enumerate some performance parameters that the design needs to reach.
If possible, please compare the actual performance realized in your design with the design parameter, and then appraise the function of Altera FPGA devices in the design.

PART IV   *Design Architecture（Preliminary Paper）：

Please give the system design scheme of your design or both hardware design block diagram and software flow chart.

PART V   Design Methodology（Final Project Paper）：

Please give the detailed description of the implementation method and steps of the design, especially how the design is implemented using the concept of SOPC.

PART VI   *Design Features（Preliminary Paper）：

Please enumerate the outstanding features of your design and what aspects of Nios helped you to implement them.

PART VII   Conclusion（Final Project Paper）：

What you learned during the design? During this contest, you certainly increased your understanding of Altera FPGA devices and made some conclusions. These conclusions will be useful for others who are learning about Altera FPGA devices or using Altera FPGA devices as reference. Please tell us what you learned during the design.